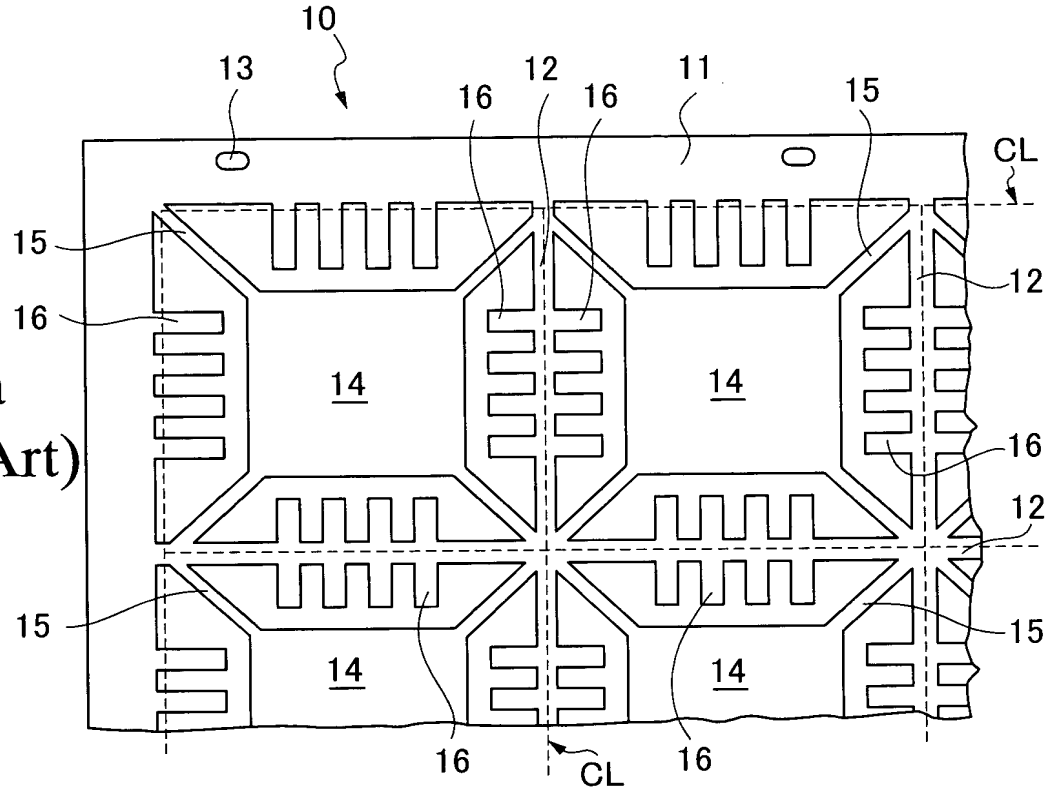



FIG. 1a
(Prior Art)

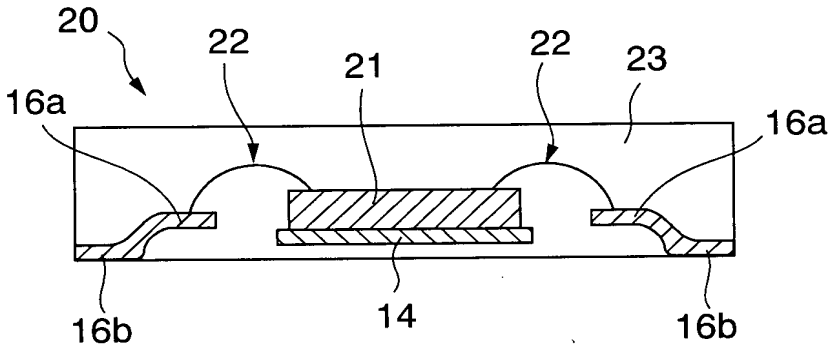



FIG. 1b
(Prior Art)

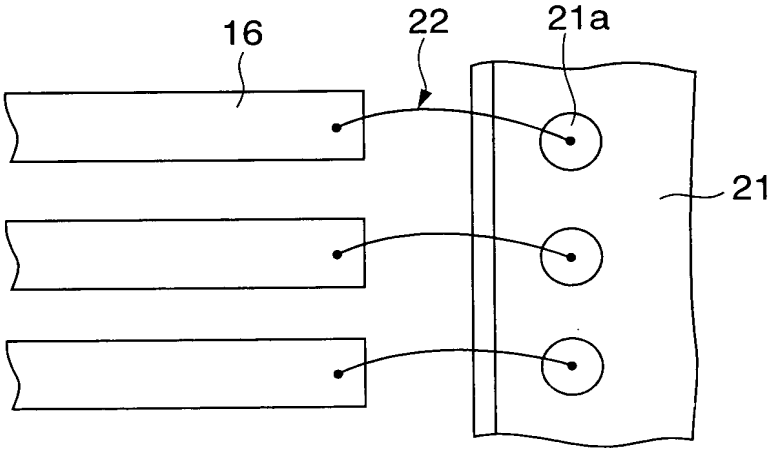



FIG. 1c
(Prior Art)

FIG. 2a

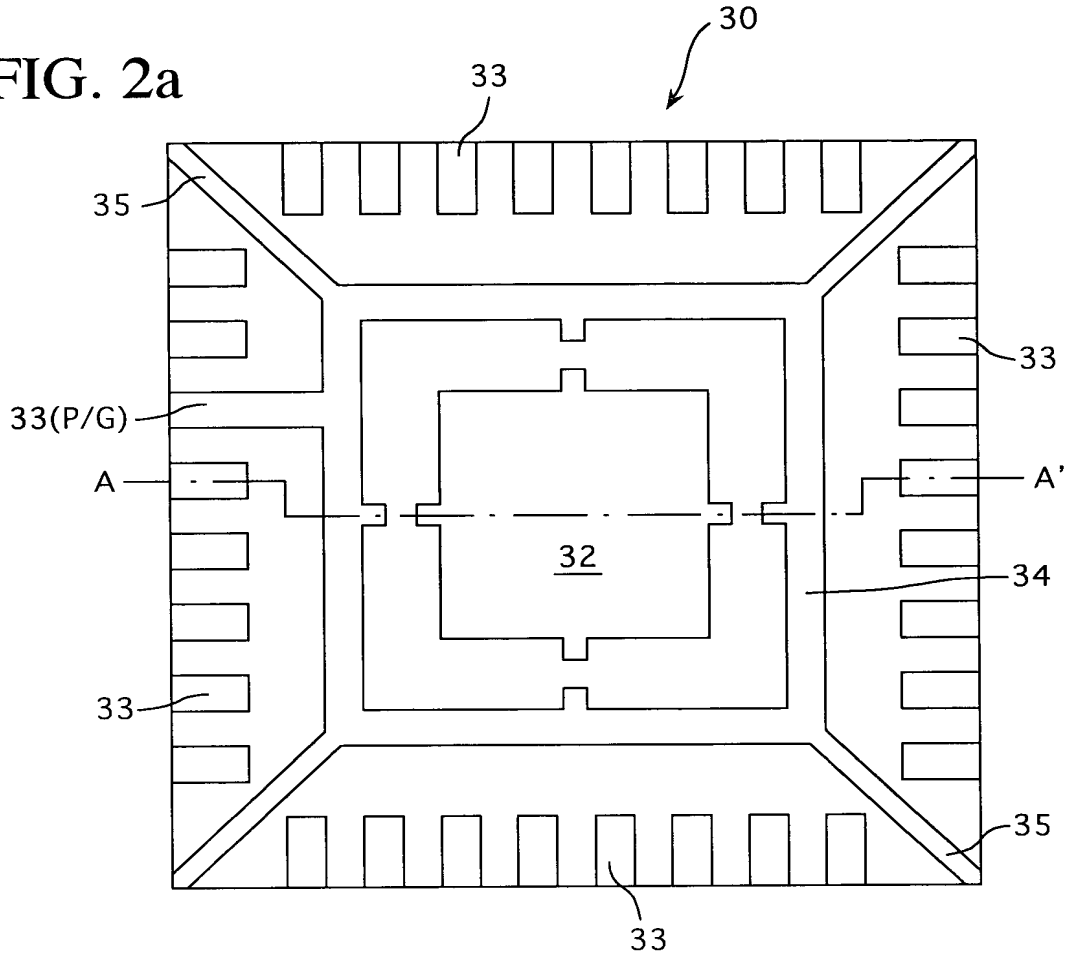
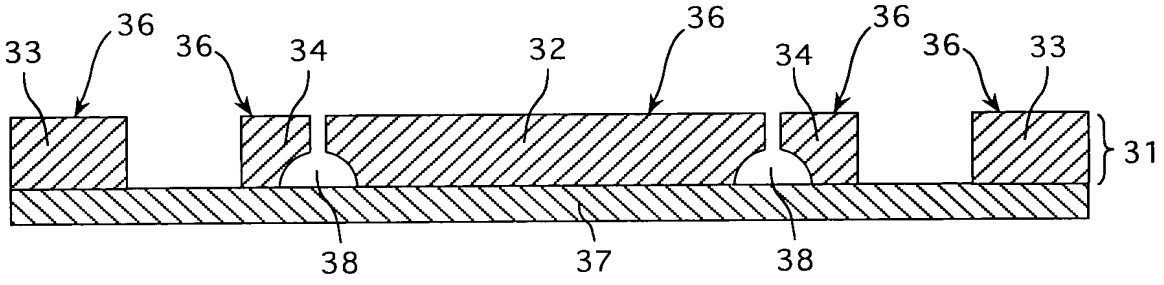

30
33
35
33
33(P/G)
A
A'
32
34
33
35
33

FIG. 2b


33
36
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37
38

FIG. 3

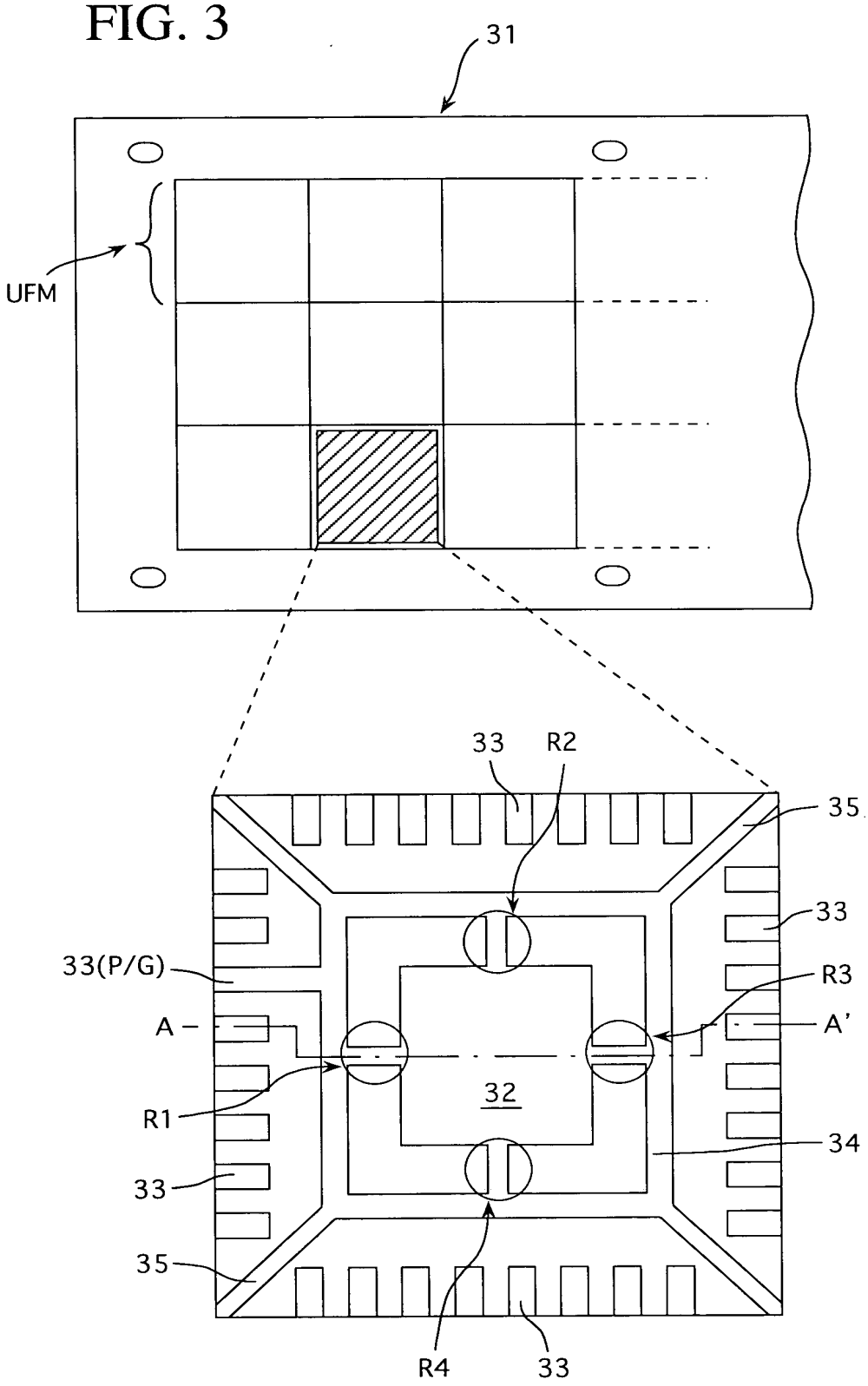


FIG. 4a

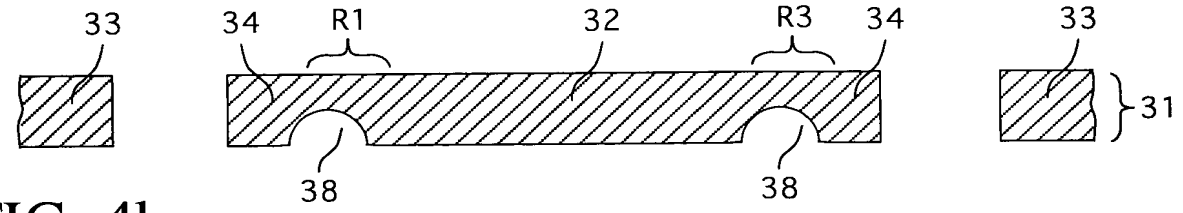



FIG. 4b

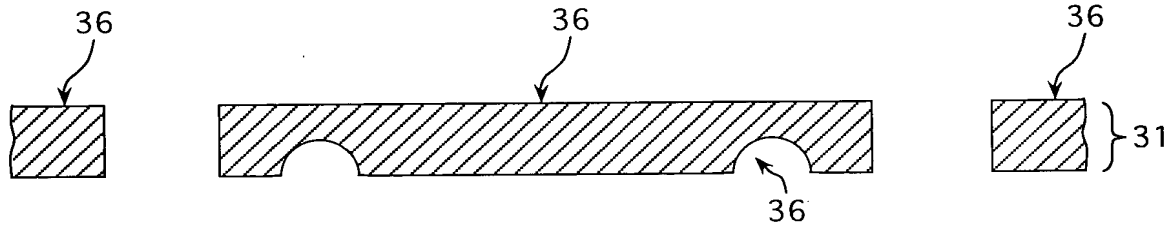



FIG. 4c

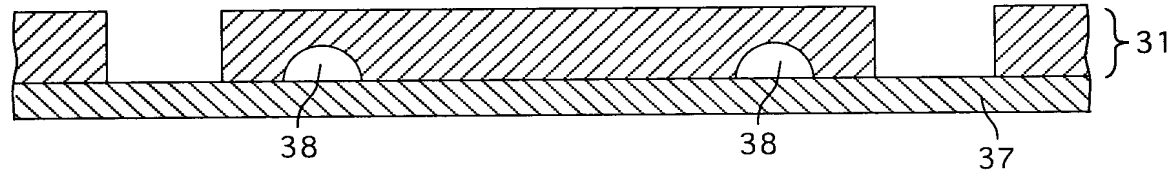



FIG. 4d

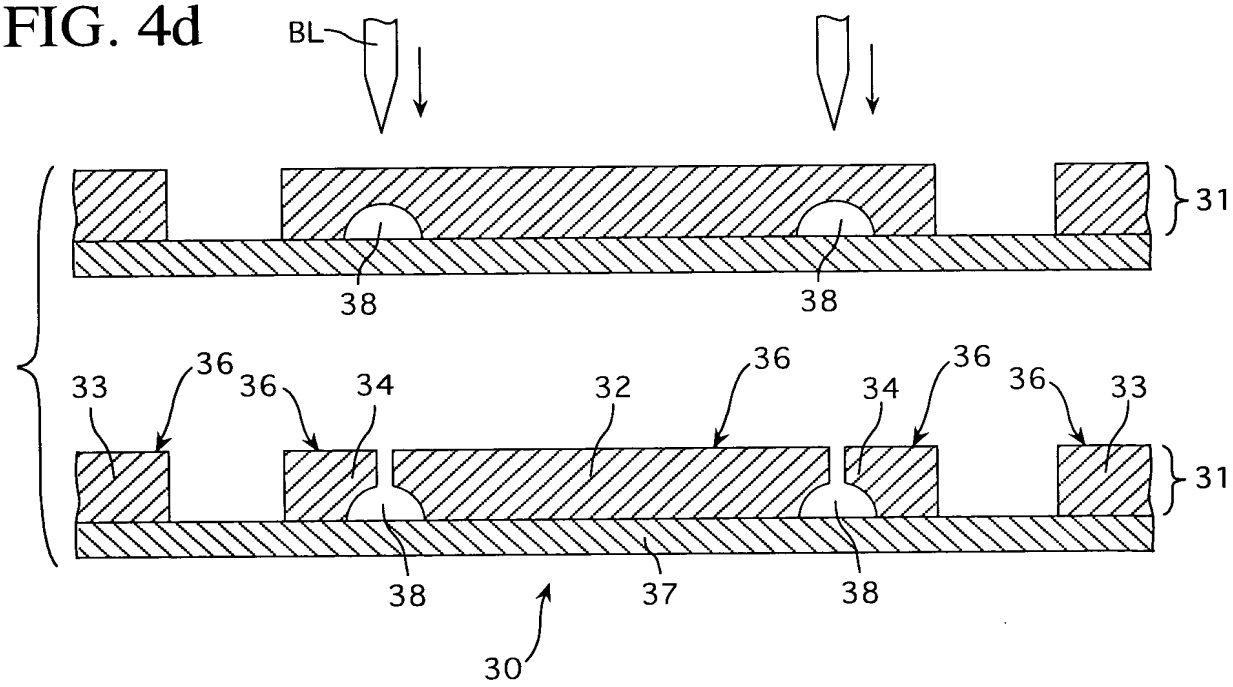


FIG. 5a

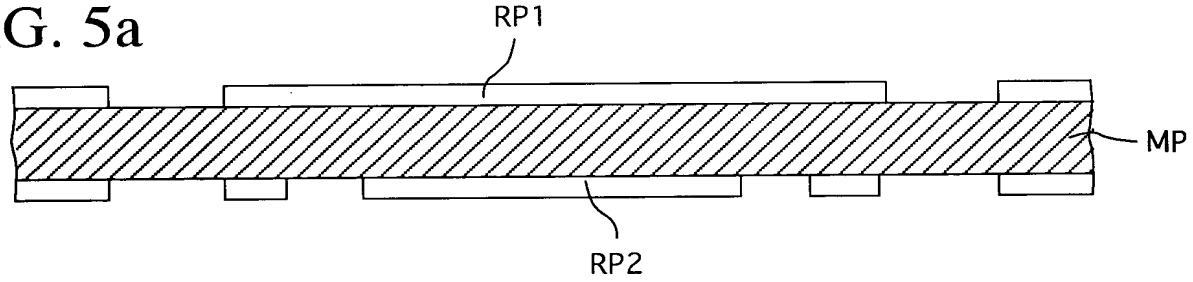



FIG. 5b

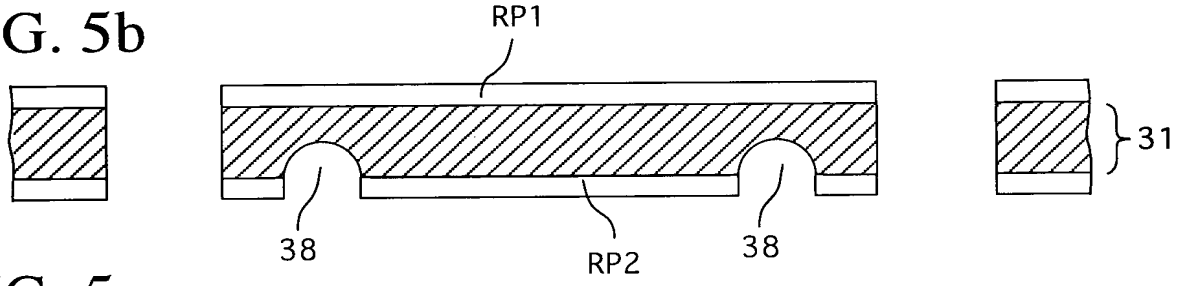



FIG. 5c

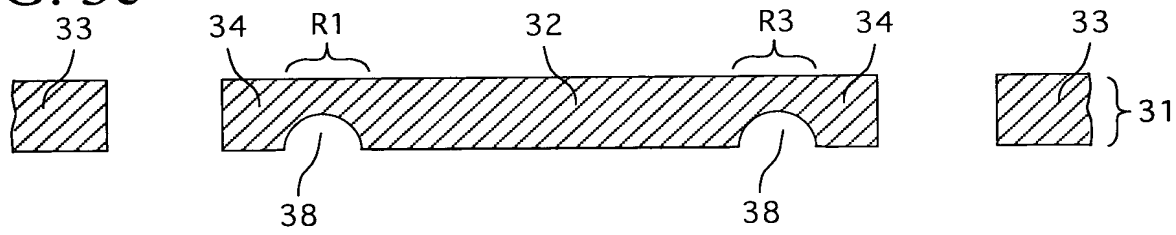


FIG. 6a

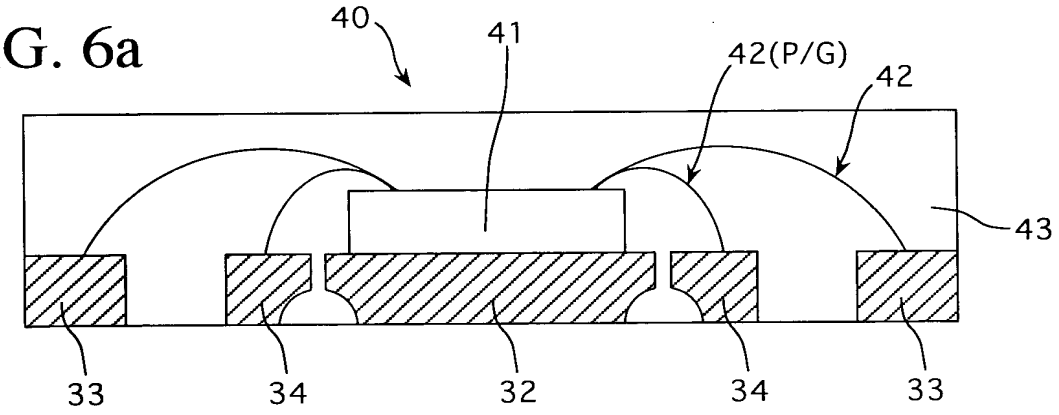



FIG. 6b

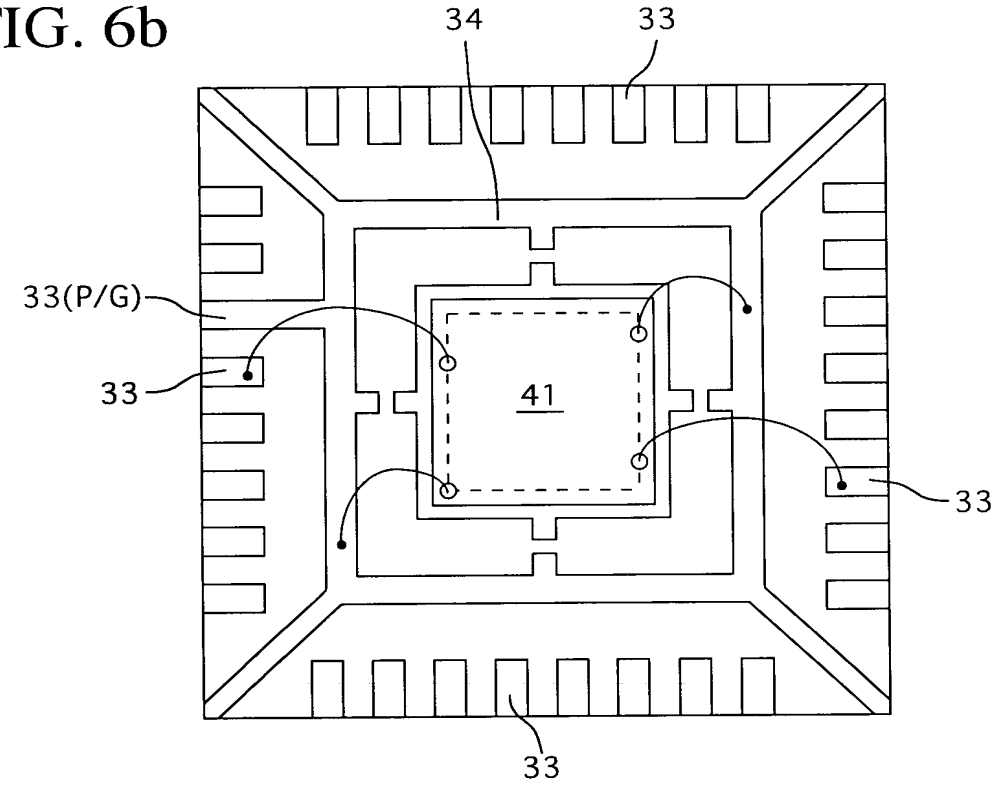


FIG. 7a

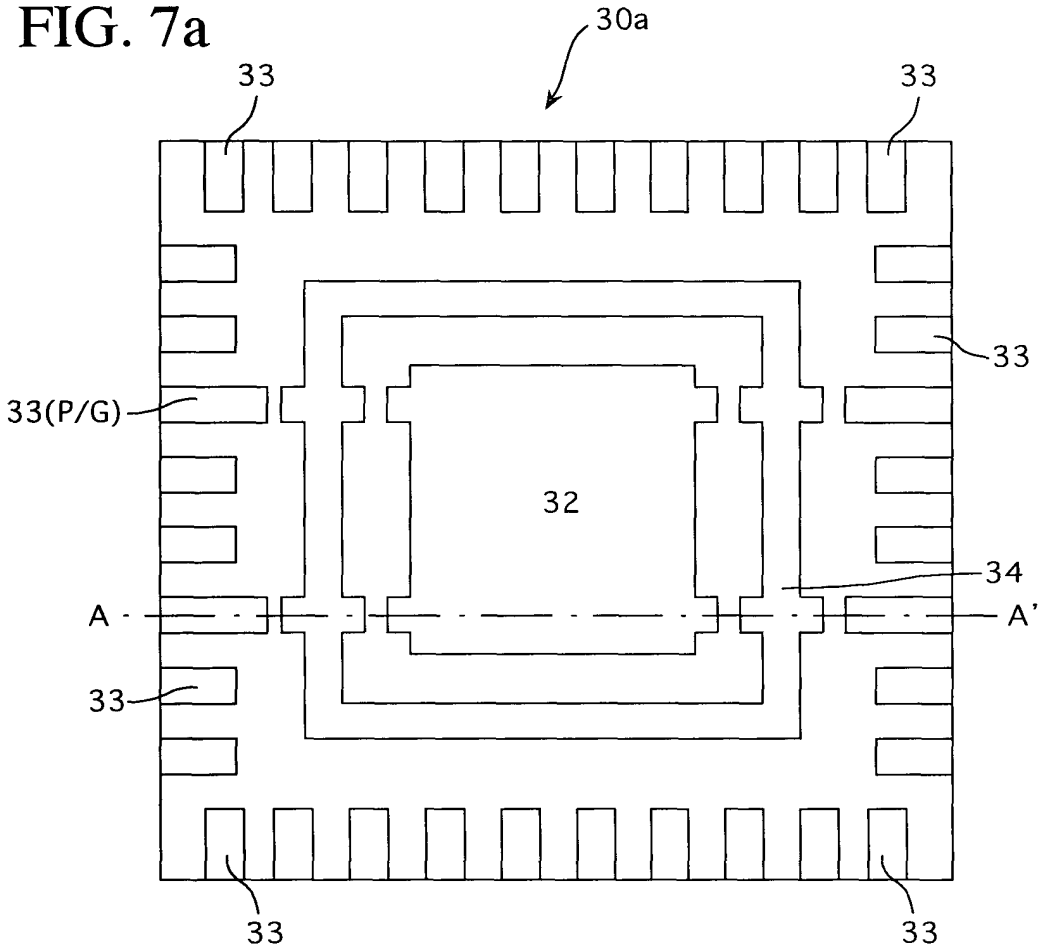
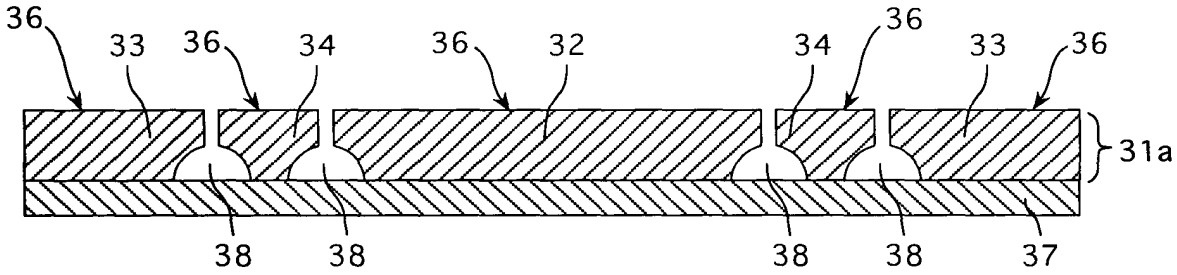

30a
33
33
33
33(P/G)
32
34
A
A'
33
33
33

FIG. 7b


36
33
36
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36
32
34
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33
36
31a
38
38
38
38
37

FIG. 8

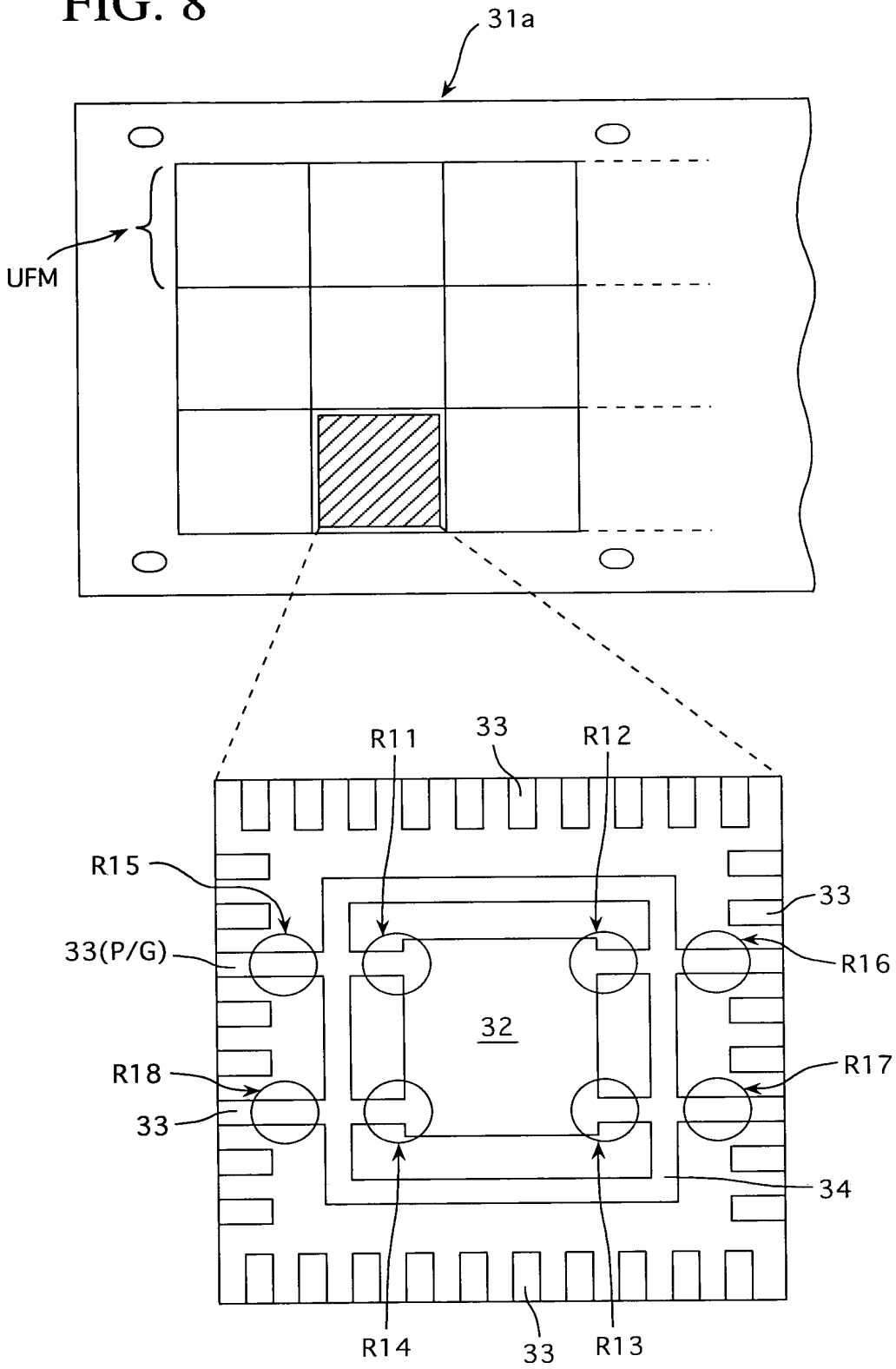


FIG. 9a

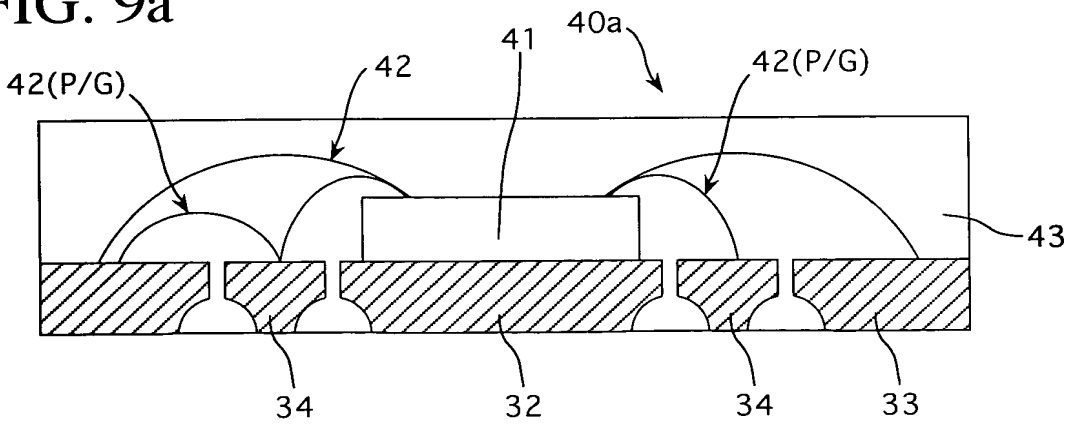



FIG. 9b

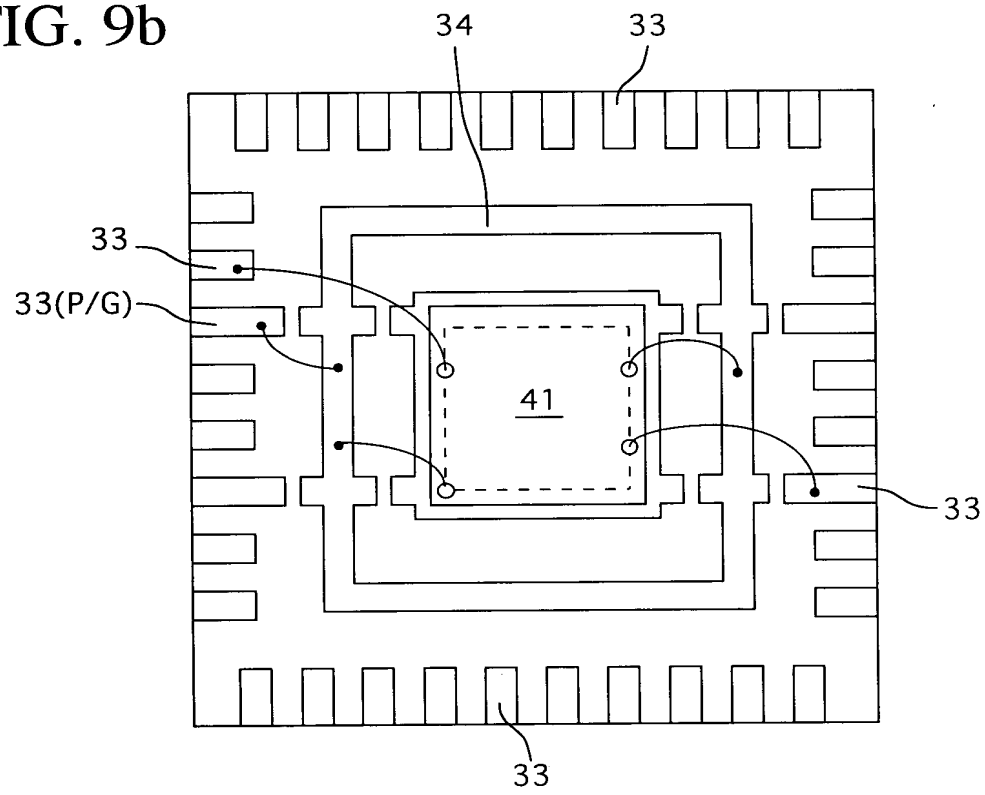


FIG. 10a

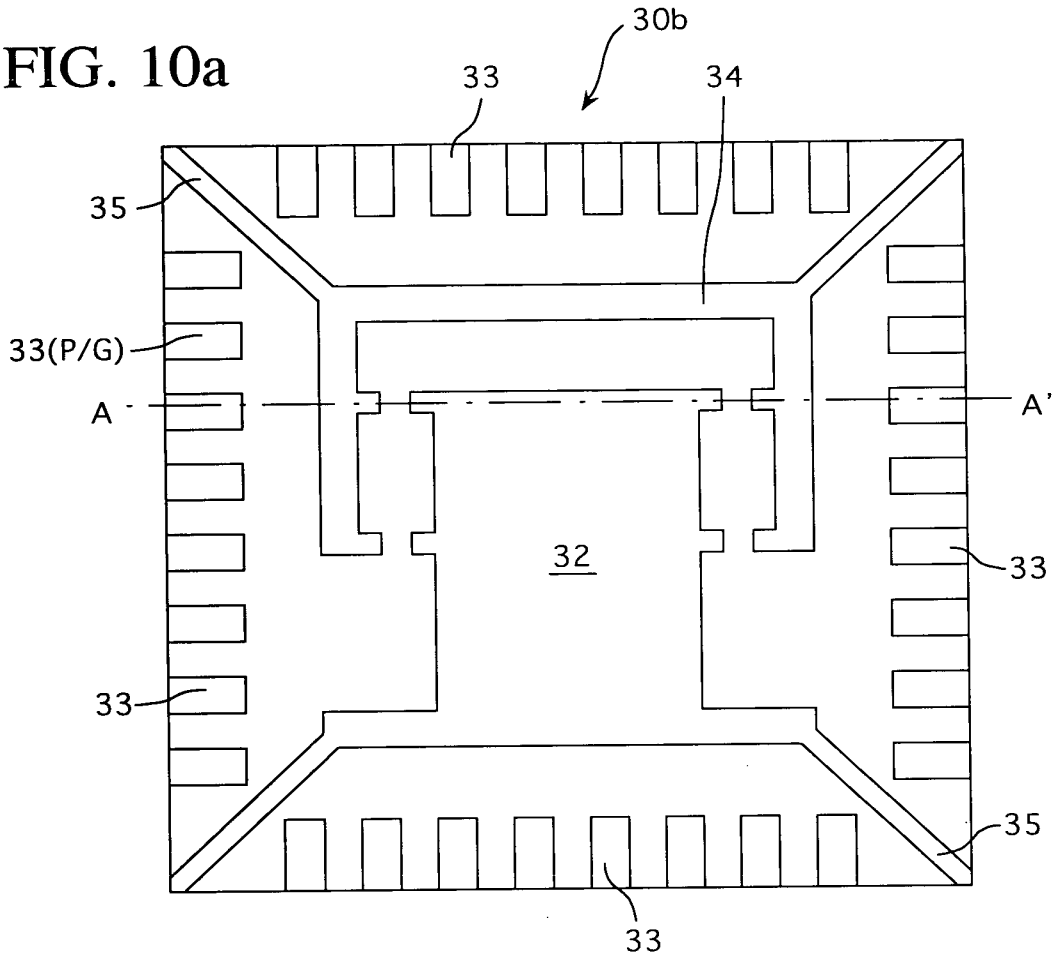



FIG. 10b

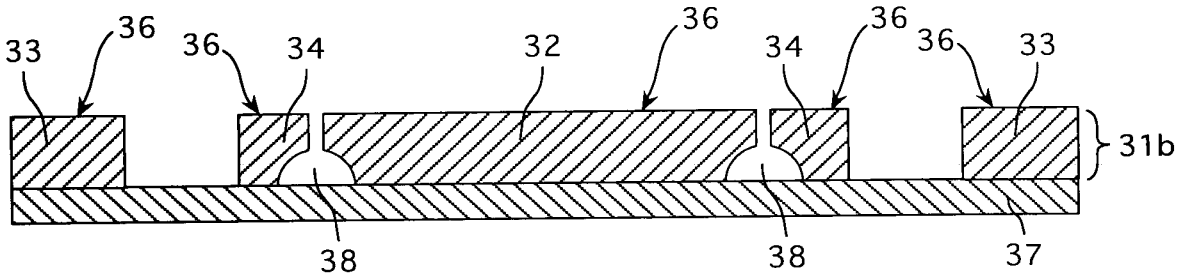


FIG. 11

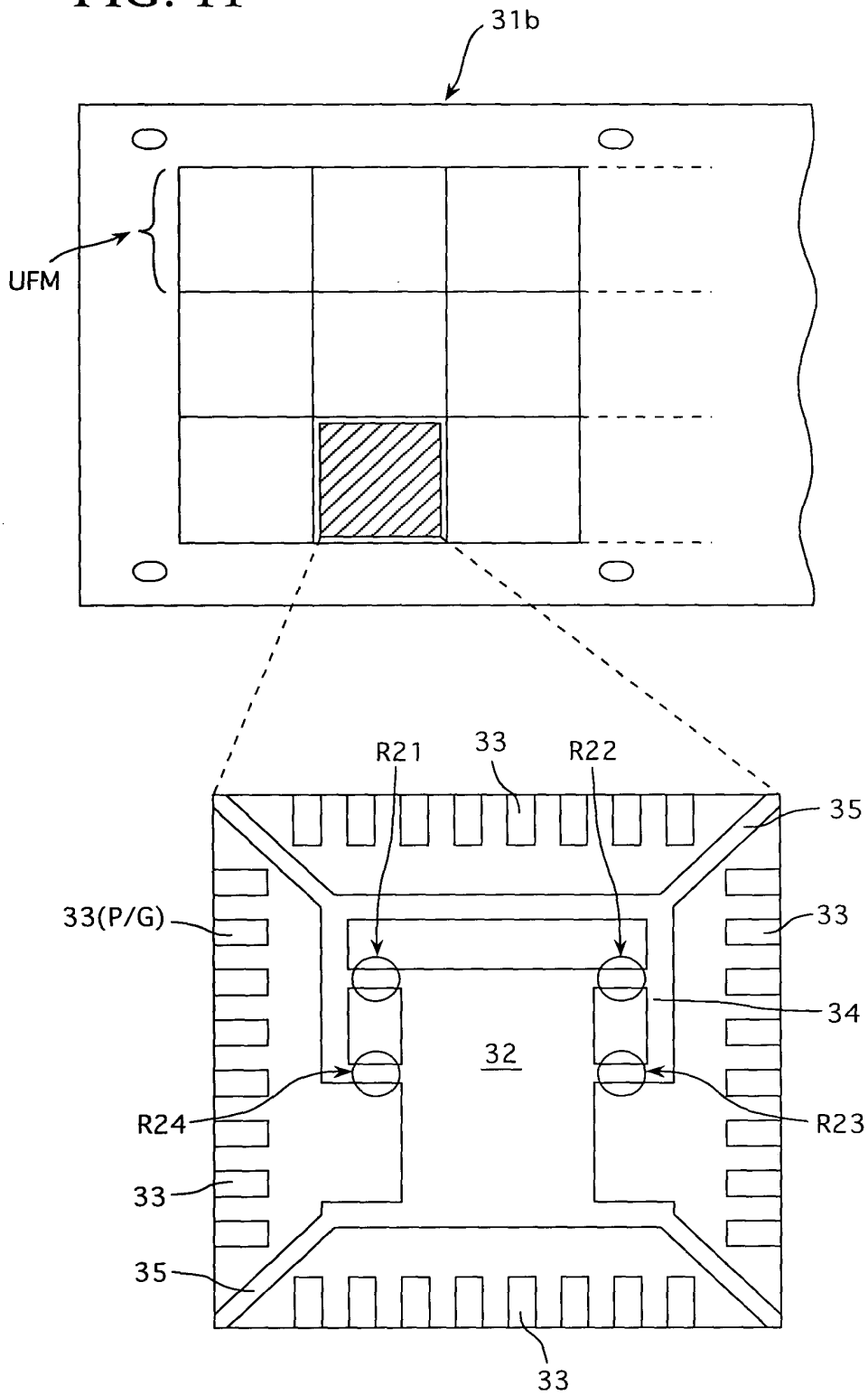


FIG. 12a

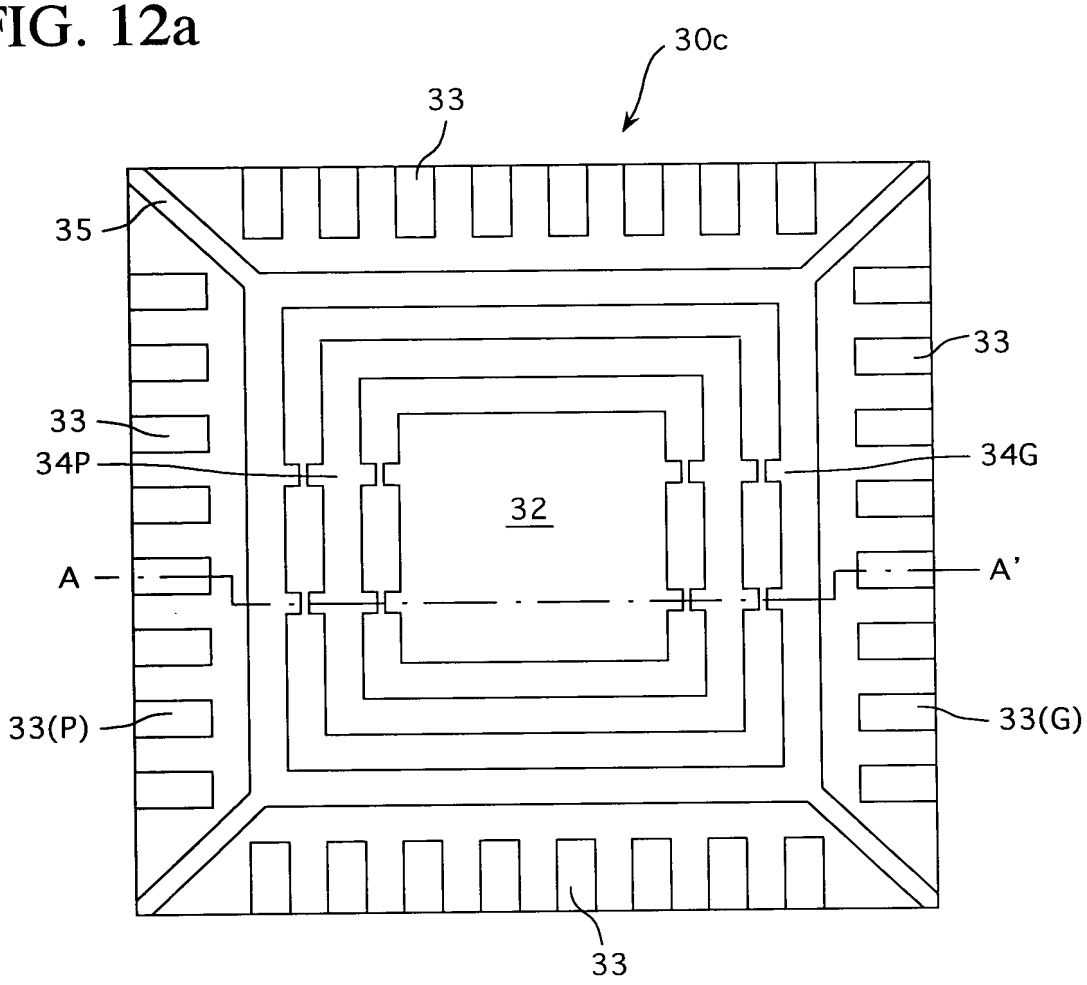



FIG. 12b

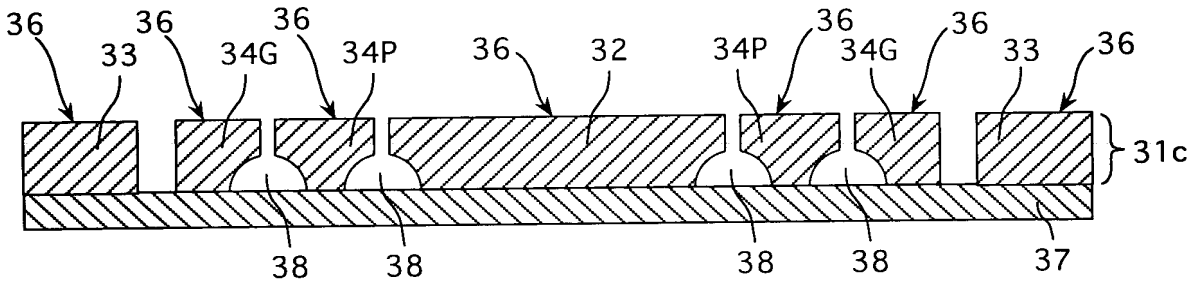


FIG. 13

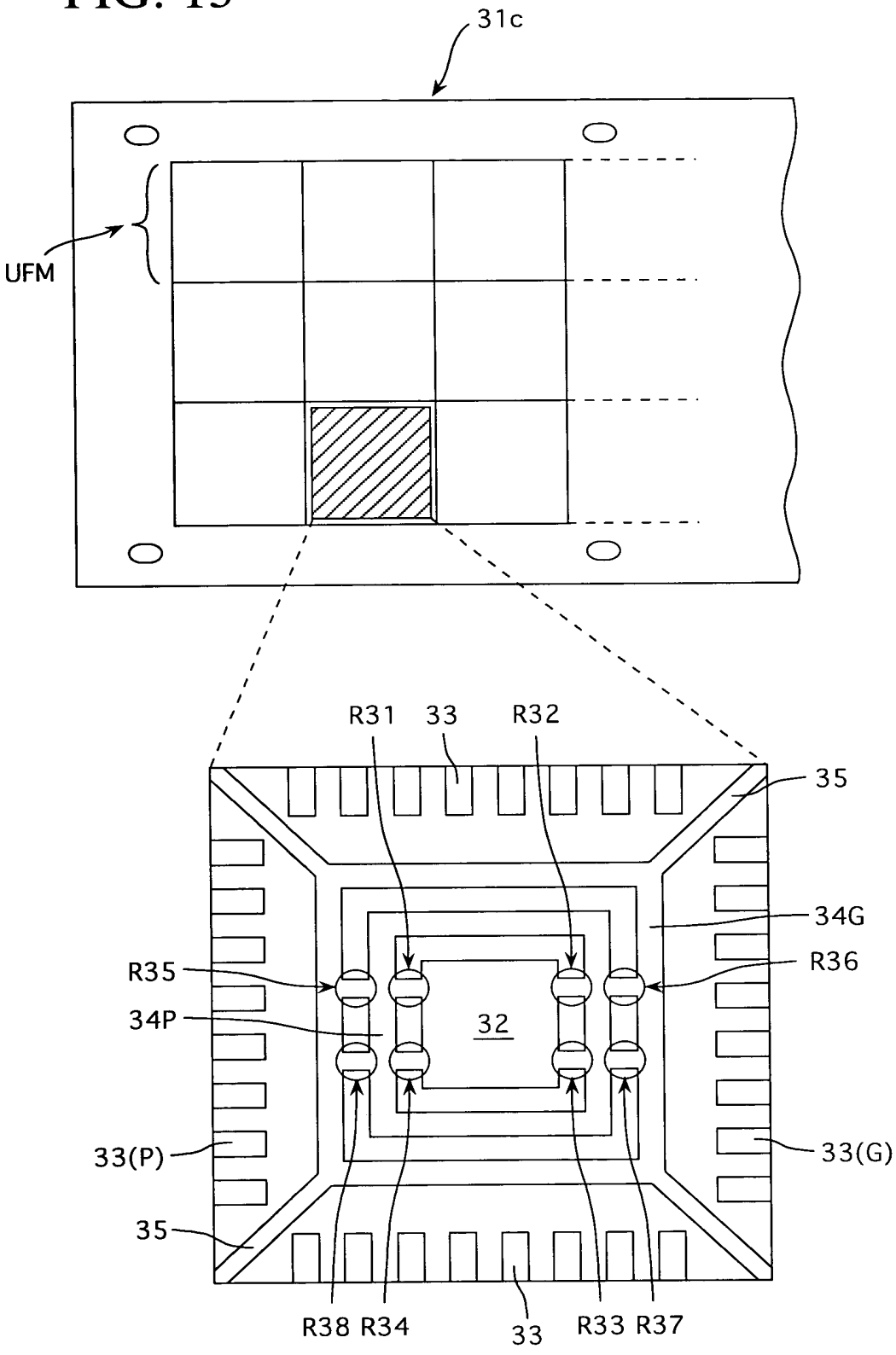

31c
UFM
R31
33
R32
35
34G
R35
R36
34P
32
33(P)
33(G)
35
R38
R34
33
R33
R37

FIG. 14a

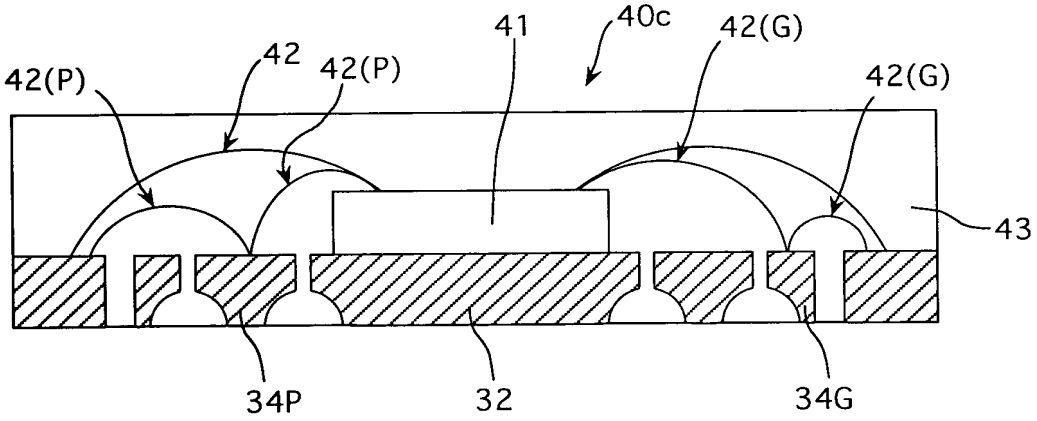



FIG. 14b

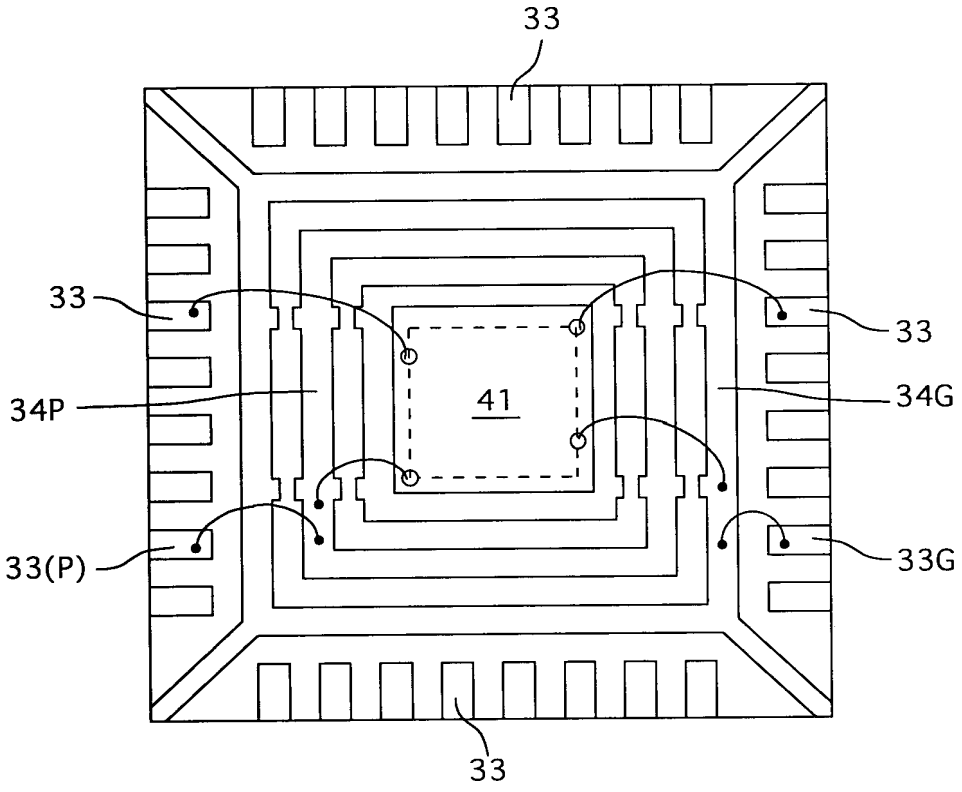
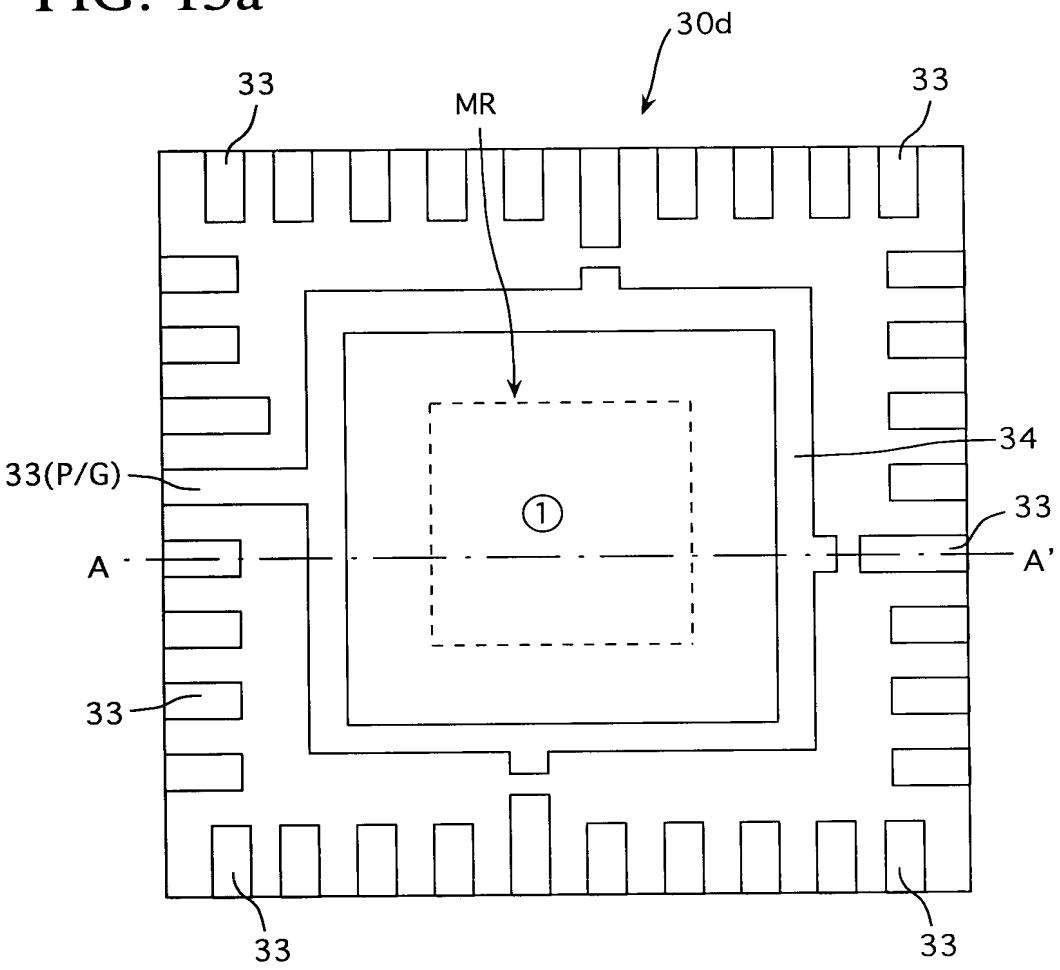


FIG. 15a




① SEMICONDUCTOR ELEMENT MOUNTING REGION

FIG. 15b

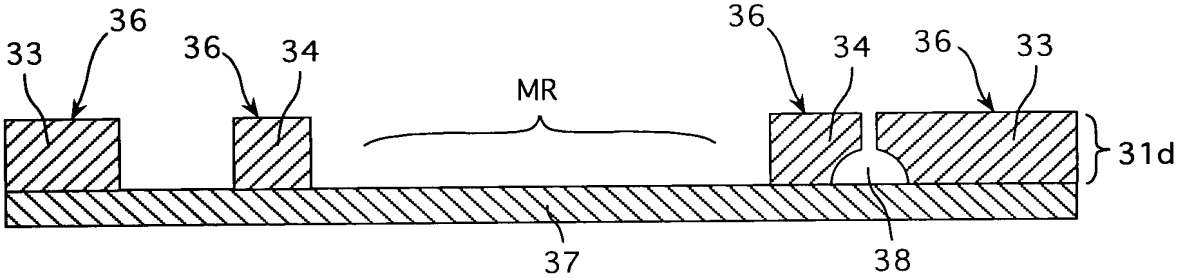
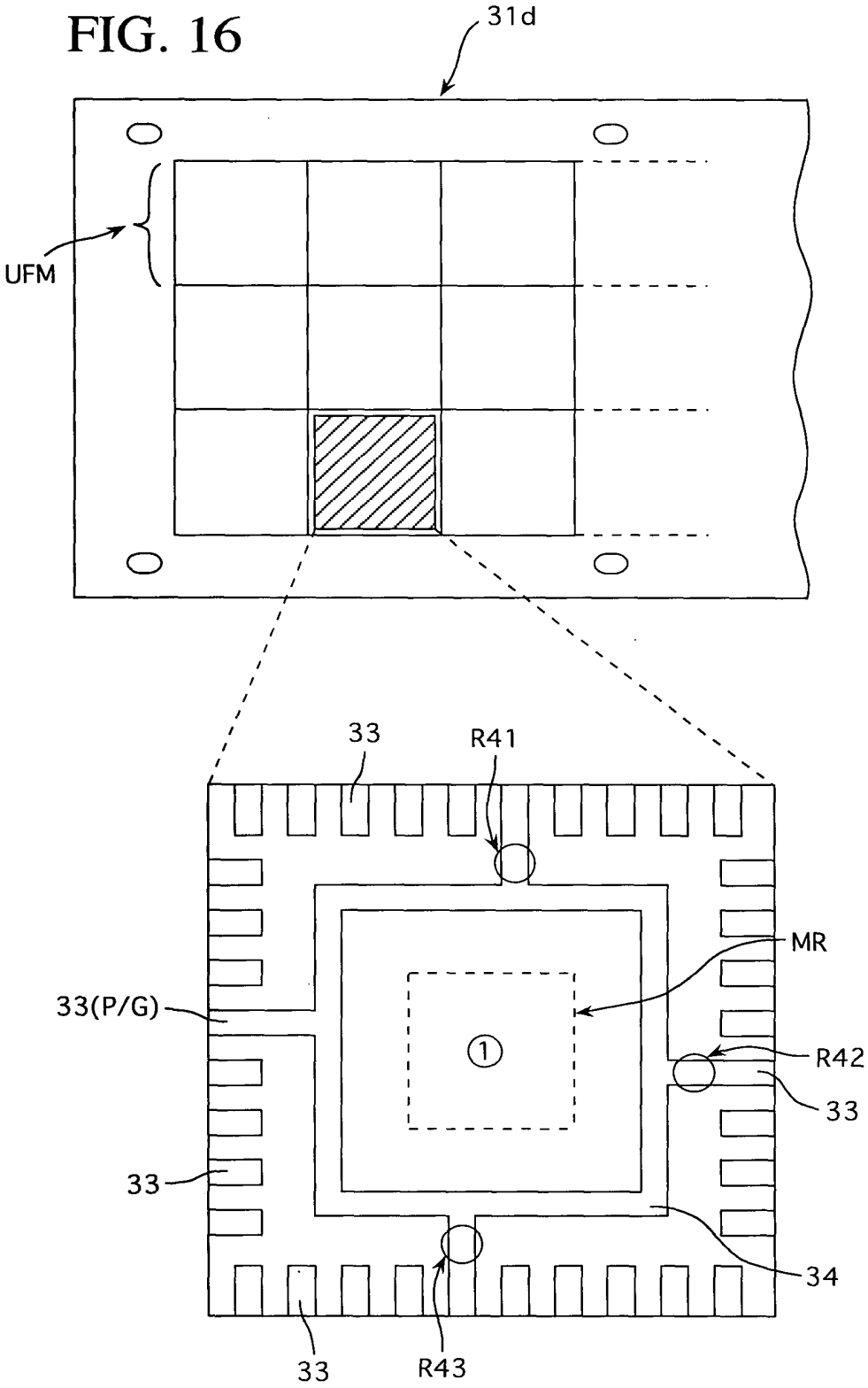


FIG. 16




① SEMICONDUCTOR ELEMENT MOUNTING REGION

FIG. 17a

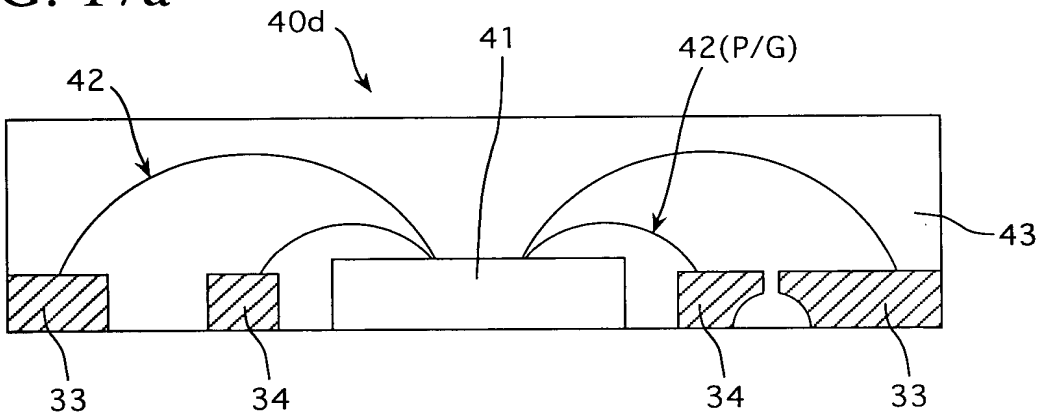



FIG. 17b